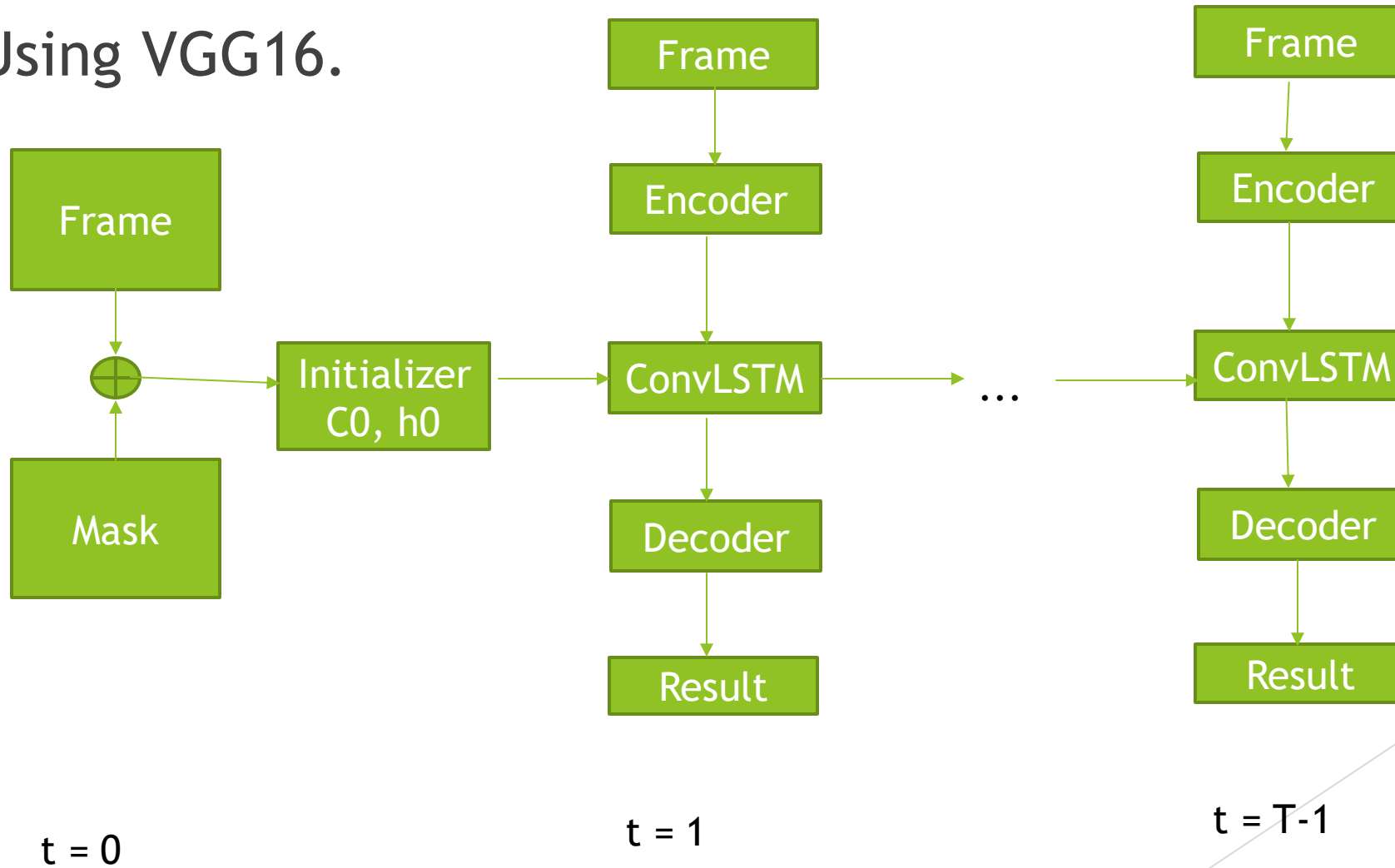


# Project 2 Presentation

By Anantapadmanaabha Prasannakumar

# Network Architecture

## ➤ Using VGG16.



# Model Details

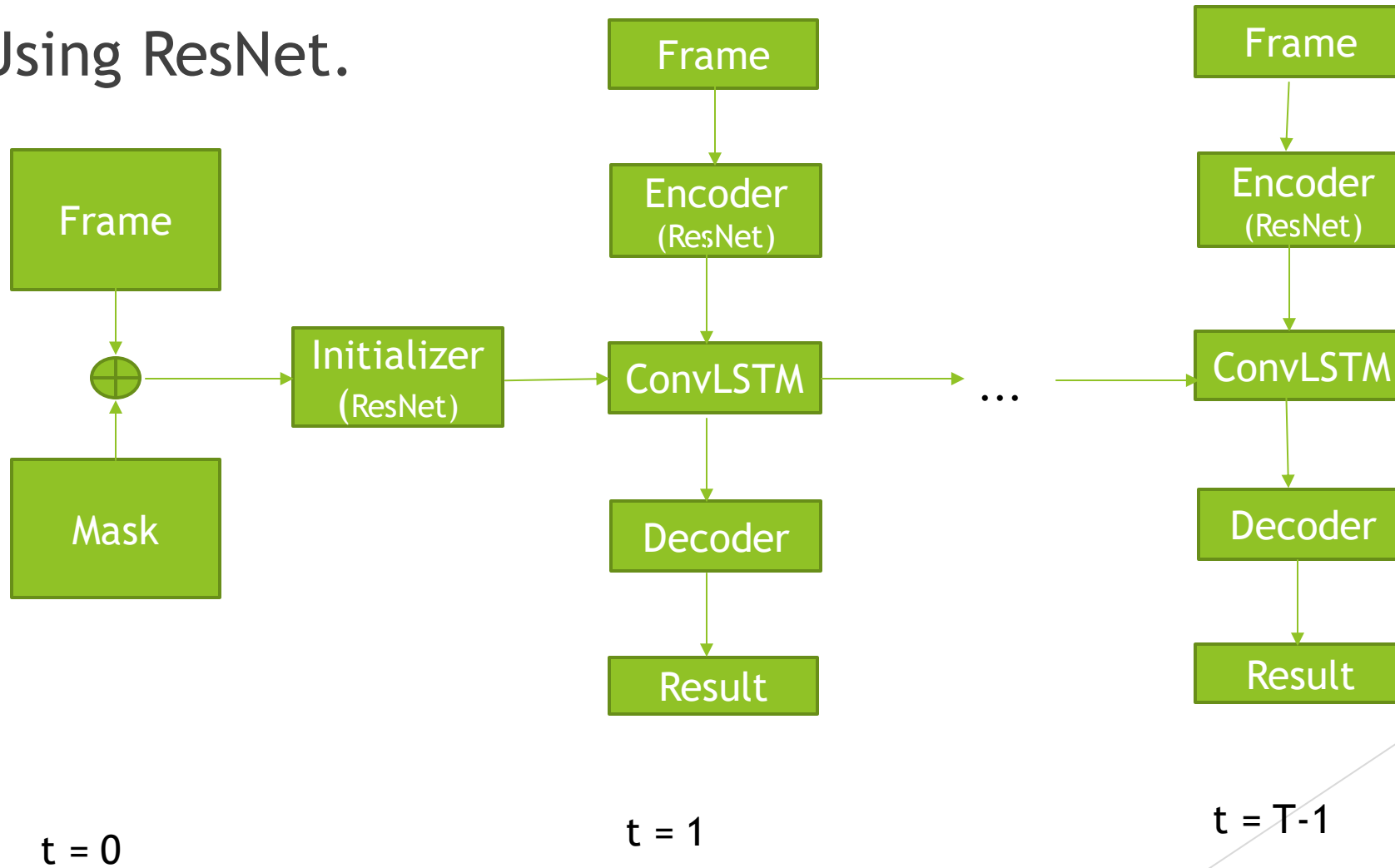
Component	Layer	Filters	Filter size	Initialization	Activation
Initializer	VGG-16 (all convolution)				
	VGG-16 (first fully-connected)		1x1		
	└─ Convolution	512	1x1	Xavier	Relu
	└─ Convolution	512	1x1	Xavier	Relu
Encoder	VGG-16 (all convolution)				
	VGG-16 (first fully-connected)		1x1		
	Convolution	512	1x1	Xavier	Relu
				Xavier	Sigmoid - gate outputs
ConvLSTM	ConvLSTM	512	3x3	Xavier Forget bias=1	Sigmoid - gate outputs Relu - state outputs
Decoder	Deconvolution	512	5x5	Xavier	
	Deconvolution	256	5x5	Xavier	
	Deconvolution	128	5x5	Xavier	
	Deconvolution	64	5x5	Xavier	
	Deconvolution	64	5x5	Xavier	
	Convolution	1	5x5	Xavier	Sigmoid

Source: Jyoti Kini Slides

[https://www.crcv.ucf.edu/wp-content/uploads/2019/04/CAP6412\\_Spring2019\\_YouTube-VOS\\_Implementation.pdf](https://www.crcv.ucf.edu/wp-content/uploads/2019/04/CAP6412_Spring2019_YouTube-VOS_Implementation.pdf)

# Network Architecture

## ➤ Using ResNet.



# Implementation Problems

- Facing problems in ConvLSTM.
- Not fully confident about my dataloader.
- Request more time.